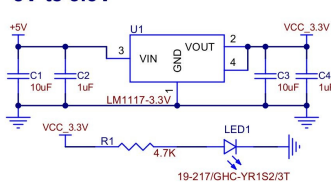
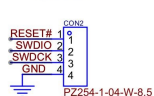


## 5V to 3.3V

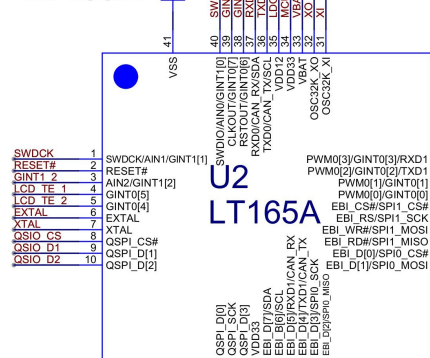


## SWD



### PCB必须预留接口或者测试点

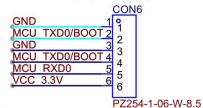
**LT165A**



如不需要RTC可去掉，VBAT接3.3V

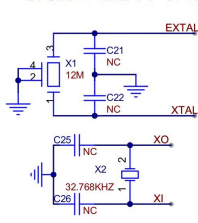
空闲可用IO口，不需要可去掉

## USART0

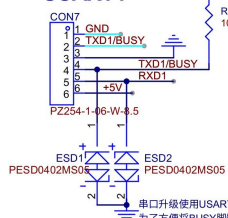


BOOT脚与TXD0共用，  
为了方便将BOOT脚短接到GND，此处再单独引出BOOT与GND脚。

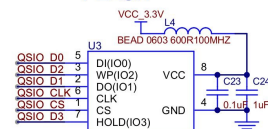
BOOT信号脚上电期间不可为低电平



## USART1



## FLASH

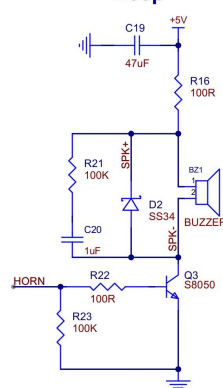


NOR : GD25Q128E/XT25F128B

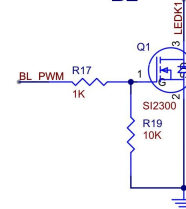
Layout时FLASH应尽量靠近LT165A

Y与GND脚。

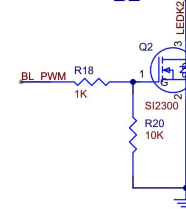
## 蜂鸣器 Beep



## BL



## BL



|           |                                                                                                                                 |      |             |                        |
|-----------|---------------------------------------------------------------------------------------------------------------------------------|------|-------------|------------------------|
| Schematic | LT165A_2LCD_SPI_V1.0                                                                                                            |      | Update Date | 2025-07-30             |
|           |                                                                                                                                 |      | Create Date | 2025-07-02             |
| Page      |                                                                                                                                 |      | Part Number |                        |
| Drawed    |  <b>乐升半导体有限公司</b><br>Levetop Semiconductor |      |             |                        |
| Reviewed  |                                                                                                                                 |      |             |                        |
|           |                                                                                                                                 |      |             |                        |
|           |                                                                                                                                 | VER  | SI ZE       | PAGE 1 OF 1            |
|           |                                                                                                                                 | V1.0 |             | Levetop Semiconductor. |